Customer No.: 31561 Application No.: 10/604,173 Docket No.: 10873-US-PA

## **Amendments**

## To the Claims:

Please amend the following claims:

Claims 1-3 (canceled)

Claim 4 (currently amended) A display driving circuit, comprising:

a plurality of driving stages, electrically coupled in serial;

a plurality of redundant stages, alternatively disposed between the driving stages and electrically coupled to adjacent driving stages, and each of the redundant stage comprises a conducting path so as to transmit an electric signal from the previous driving stage to the next driving stage, wherein the redundant stage and the driving stage are electrically connected in serial, and the redundant stage is the driving stage installed with a redundant device; and

a plurality of driving lines, wherein each of the driving lines corresponds to one of the driving stages or the redundant stages respectively, and each of the driving line is electrically coupled to an output terminal of a corresponding driving stage or a corresponding redundant stage.

Claim 5 (previously presented) The display driving circuit of claim 4, wherein each of the redundant stage includes a driving stage and a redundant device.

Claim 6. (previously presented) The display driving circuit of claim 4, wherein each pair of two adjacent redundant stages further comprises at least one another driving stage electrically coupled therebetween.

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Claim 7 (previously presented) The display driving circuit of claim 5, wherein the redundant device comprises a plurality of transistors electrically coupled in parallel with transistors in the driving stage.

Claim 8. (previously presented) The display driving circuit of claim 7, wherein the redundant device is capable of supplying an extra conducting path to transmit an electric signal from the previous driving stage to the next driving stage via the current redundant stage while the original conducting path in the corresponding driving stage of the redundant stage is broken.

Claim 9. (previously presented) The display driving circuit of claim 7, wherein the driving stage comprises six transistors.

Claim 10 (currently amended) A display driving circuit, comprising:

a plurality of driving stage groups, electrically coupled in serial, and each of the driving stage groups comprises a plurality of driving stages electrically coupled in serial;

a plurality of redundant stages, alternatively disposed between the driving stages group and electrically coupled to adjacent driving stages group, and each of the redundant stage comprises a conducting path so as to transmit an electric signal from the previous driving stage group to the next driving stage group, wherein the redundant stages and the driving stage groups are electrically connected in serial, and the redundant stage is the driving stage installed with a redundant device; and

a plurality of driving lines, wherein each of the driving lines corresponds to one of the driving stages or the redundant stages respectively, and each of the driving line is electrically

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coupled to an output terminal of a corresponding driving stage or a corresponding redundant stage.

Claim 11 (previously presented) The display driving circuit of claim 10, wherein each of the redundant stage includes a driving stage and a redundant device.

Claim 12 (previously presented) The display driving circuit of claim 11, wherein the redundant device comprises a plurality of transistors electrically coupled in parallel with transistors in the driving stage.

Claim 13. (previously presented) The display driving circuit of claim 12, wherein the redundant device is capable of supplying an extra conducting path to transmit an electric signal from the previous driving stage to the next driving stage via the current redundant stage while the original conducting path in the corresponding driving stage of the redundant stage is broken.

Claim 14. (previously presented) The display driving circuit of claim 12, wherein the driving stage comprises six transistors.

Claim 15 (previously presented) The display driving circuit of claim 10, wherein the driving stage group includes N number of a plurality of driving stages, and the redundant stage is electrically connected subsequent to the driving stage group.